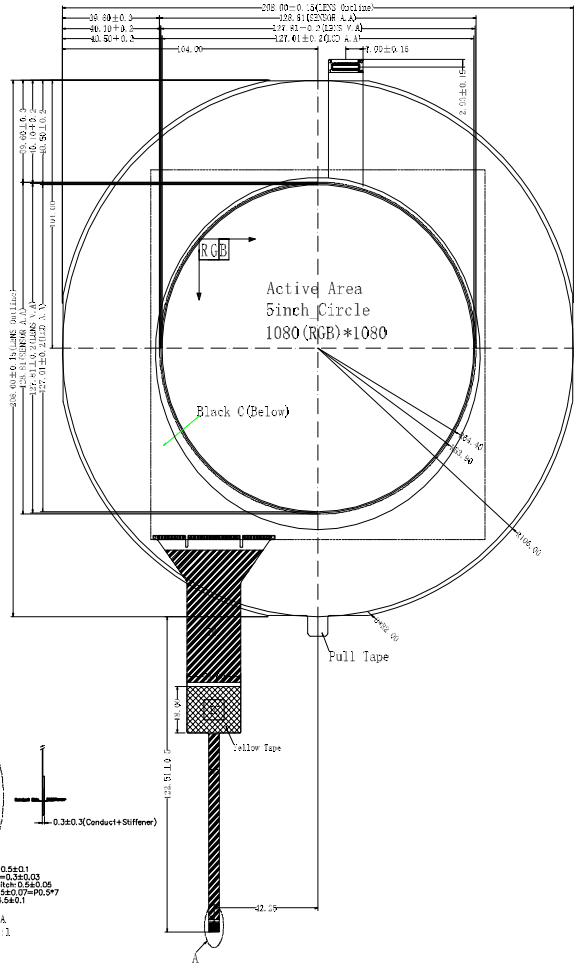
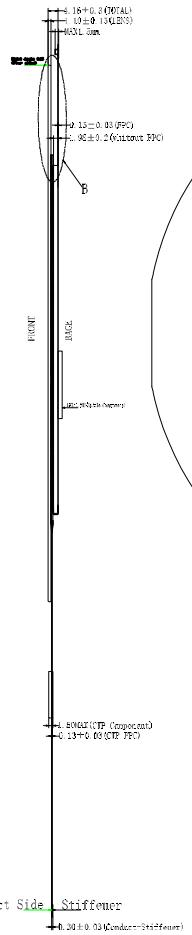
ALL PAGES OF THIS VERSION APPROVED			
SIGNATURE:		DATE:	

REV.	Description	Date
01	New released	

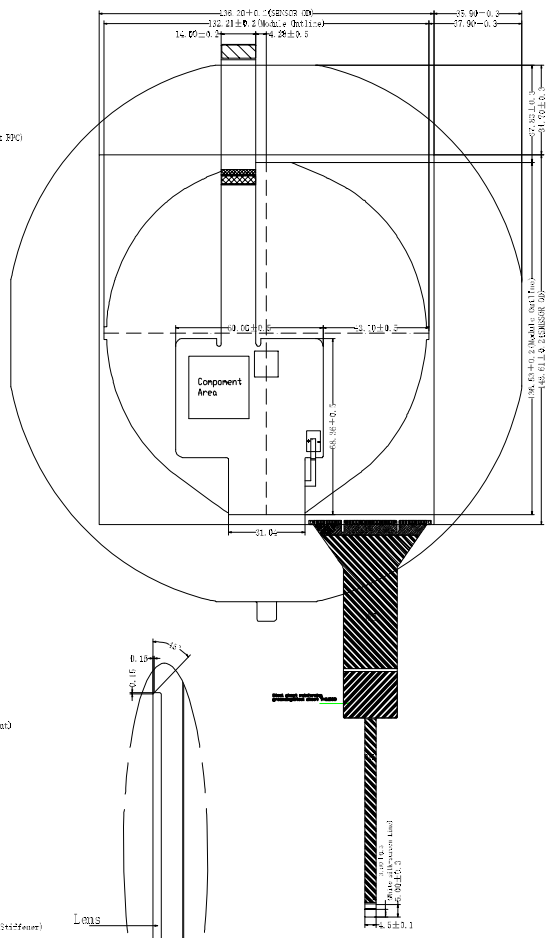
Front View

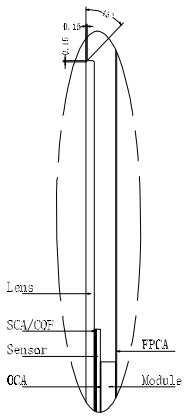


Side View



Rear View



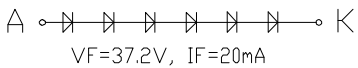


Pin	Signal	Function	Pin	Signal	Function
49	GND	Ground	50	GND	Ground
47	GND	Ground	48	RESET	Disp reset
45	NC	NC	46	TE	Disp tearing effect out
43	NC	NC	44	LEDPWM	Backlight pwm output
41	NC	NC	42	GND	Ground
39	NC	NC	40	NC	NC
37	GND	Ground	38	VDD(+5)	Display +5V analog rail
35	NC	NC	36	VDD(+5)	Display +5V analog rail
33	NC	NC	34	NC	
31	GND	Ground	32	VDD(-5)	Display -5V analog rail
29	LAN3_N	Disp MIPI lane 3-	30	VDD(-5)	Display -5V analog rail
27	LAN3_P	Disp MIPI lane 3+	28	NC	NC
25	GND	Ground	26	GND	Ground
23	LAN0_N	Disp MIPI lane 0-	24	IOVCC	Disp digital power
21	LAN0_P	Disp MIPI lane 0+	22	IOVCC	Disp digital power
19	GND	Ground	20	GND	Ground
17	CLK_N	Disp MIPI CLK -	18	TD_PIN1	TD pd to GND(BOE)
15	CLK_P	Disp MIPI CLK +	16	TD_PIN2	TD pu to 1.8V(BOE)
13	GND	Ground	14	GND	Ground
11	LAN1_N	Disp MIPI lane 1-	12	LEDA	LED Anode
9	LAN1_P	Disp MIPI lane 1+	10	LEDA	LED Anode
7	GND	Ground	8	GND	Ground
5	LAN2_N	Disp MIPI lane 2-	6	LEDK	LED cathode
3	LAN2_P	Disp MIPI lane 2+	4	LEDK	LED cathode
1	GND	Ground	2	GND	Ground

FPC Logic

PIN	Logic
1	GND
2	VDDIO
3	VDD
4	SCL
5	SDA
6	INT
7	RST
8	GND

NOTES:
1. DISPLAY TYPE: 5.0", TFT-LCD, 16.7M COLORS
2. DISPLAY MODE: ADS Normal Black
3. VIEWING DIRECTION: Free
4. TFT DRIVER IC: HX8399C
   CTP DRIVER IC: ST1727
5. Touch type: CTP
6. TFT Interface: MIPI
7. CTP Interface: I2C
8. Touch and LCM Bonding technology:Optical Cement
9. TFT VDD:+5.0V/-5.0V;CTP VDD:3.3V;VDDIO:1.8~3.3V
10. OPERATING TEMP: -20°C TO 60°C
    STORAGE TEMP: -30°C TO 70°C
11. BACK LIGHT: LED WHITE, 6 LED,
12. CONNECTOR: DF40C-50DP-0.4V(51), Hirose
13. RoHS COMPLIANT.



VF=37.2V, IF=20mA
BACKLIGHT CIRCUIT

特殊工艺:

MACLIGHT DISPLAY CO., LIMITED

GENERAL TOL 0.30	ALL UNITS: MM	DATE	MODEL NUMBER : MLT050F50-CT1		SHEET: 1 OF 1
DWN:					
CHK:					DATE:
APP:			DO NOT SCALE THIS DRAWING.	PROJECTION	